

SF400

* + , -

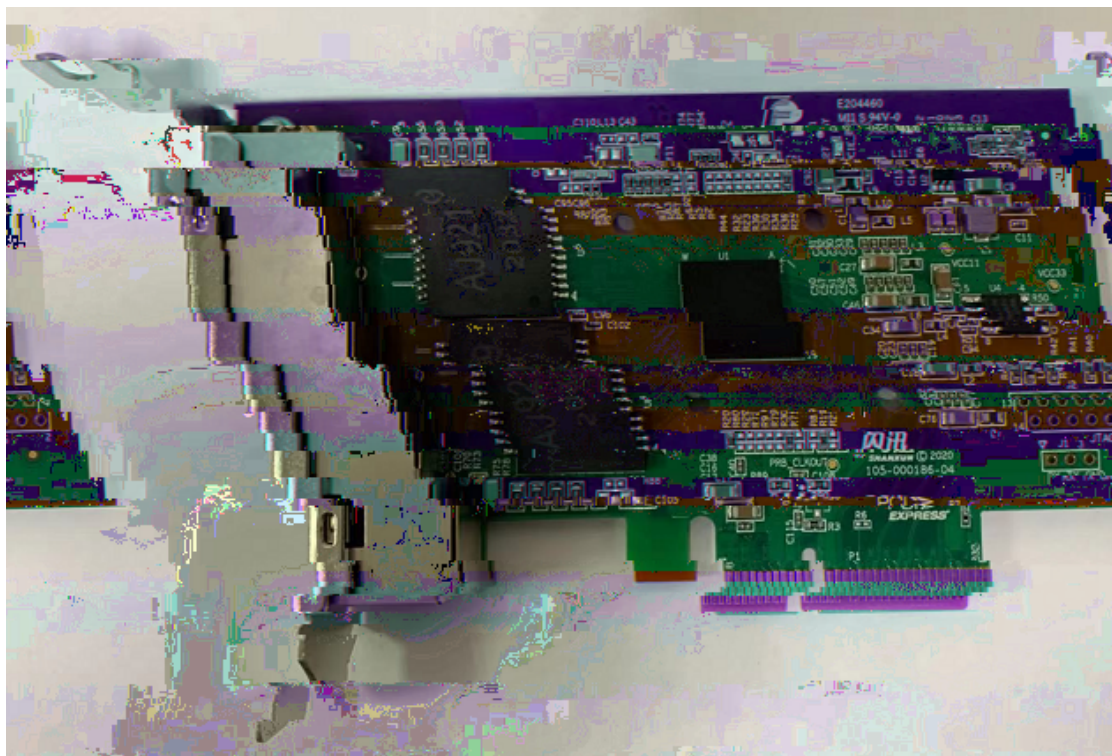
! " # \$ % & ' () * + , - . / 0 1 2 \$ 3 4 5 6 7 8 9 : ; - . / 0 ;
\$ ' < = - . / 0 > ? ' @ A B C D E F G H I J ' K L ' M N O P @ Q R S T

) * # \$ G H U V

	SF400 WX 7Y / Z [\ S] ! "			
	V1.00			2019.11
V1.00				2019.11.22
V1.01				2020.6.1
V1.02				2020.7.3
V1.03	WOL NCSI			2021.1.7
V1.04	SF400 PCB 1.03 1.04			2021.6.1
V1.05				2021.6.23
V1.06				2021.12.21

. 1 / SF400 O1 23" " 456789

SF400 WX7Y//^Z[_ , - . /O12\$345`abcdWX7Y/eJ
 \fg'h\$`aijk*'lmnopqrstuv/^wxyz' {S|}~\ '
 rs m' \$ / ^ | CPU ' rs D
 CPU S ' | CPU \ t d mT
 SF400 WX7Y//^Z[\ \$ d m ' 100 / ' Z
 S, }~\D b 'I? @A PCI Express WX/^t' v m
 ' I/O G / ^ {Sd T
 SF400 WX7Y//^Z[\ 7Y/ MAC+PHY6 eJ\D
 \;' |WXC7Y/ tw ? > E 7 d 7Z{
 m/^ ' ?7 ?7 PCI Express ' G
 S T
 SF400 WX7Y//^Z[\ 8



1.1 : ; < = % > ? @

—	
k	SF400T/SF400HT/SF400HXT(S/ S/ S)
	10Base-T Category 3' 4' 5' 5E or6 w 100m
	100Base-Tx Cat5 Cat 5E and Cat6 w 100m
	1000Base-T Cat 5E and Cat6 w 100m
	4E RJ45
k	3W
PCIe	PCI Expressv2.1(5.0GT/s) x4 &x8 x16
	10Mbps/100Mbps/1000Mbps ()
	Core 1.1V _{+3%} ' 10 3.3V _{+3%}
2 r	
7Y/	Yes
IEEE802.3` Z {	Yes
S	10BASE-T, 100BASE-TX, 1000BASE-T
	IEEE 802.3 az IEEE 802.3u 100Base-TX Fast Ethernet IEEE 802.1Q VLAN IEEE 802.1ad IEEE 802.3ab
(?)	SM2/SM3/SM4
PCI-SIG	Yes
	Yes
b (VMDq)	Yes
PXE	Yes
FCoE	NO
WOL	NO
NCSI	NO
LED	
LED	1000/100M D ACT ' 100M ' 1000M ' \$ r s ACT ' T

. 2 /] ^ _ `

2.1 6abS

Ø SF400WX7Y//^Z[\

Ø \$%

Ø &'

Ø (1E' (1E

2.2 cdef

&%7 SF400WX7Y//^Z[\m) * +, - . / O7812 T

Ø 34c T

Ø 56 T

Ø 78a 8 9 : dPCI-E6X8 X16; ; T

Ø 78<=>O 6?@A B; 8 6 C\$; T

Ø DE7WX7Y//^Z[\dFGHI (JK B ; T

Ø S \$d<>O?@LxKMWX7Y//^Z[\dKM (T

Ø 8 a 8' B 6Nc T

2.3 gX] ^hi

O - PQ/OWX/ RS ST UT

. 3 / j kl mnN

Ø k +, VQWX &YT

Ø Z[k \] t' ^ _

Ø W[`a bcd' ef gf hi j f_

Ø -kl `m, n7 o' T

Ø -k pq' b drsD tu ?m vwT

Ø -k k xy' b drsD tu ?m vwT

Ø W[z{ ' rGz{ ?m vwk T

Ø S S _| 0% } 90%' T